Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	465	asymmetric adj data	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53
L2	114	(asymmetric adj data) and @pd<"20000921"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53
L3	33	((asymmetric adj data) and @pd<"20000921") and asymmetric.ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53
L4	0	dual near (clock adj edge) near sampl	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53
L5	1	dual adj edge adj sampling	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53
L6	458	dual adj edge	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53
L7	131	dual adj edge and sample	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53
L8	45	(dual adj edge and sample) and @pd<"20000921"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53
L9	34	dual adj data adj rate and sample	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53

L10	135	710/66.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53
L11	80	710/66.ccls. and @pd<"20000921"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53
L13	4074	double adj data adj rate	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53
L14	180	(double adj data adj rate) and 32$bit and 64$bit	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53
L15	0	((double adj data adj rate) and 32$bit and 64$bit) and @pd<"20000921"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53
L16	2522	32$bit and 64$bit and @pd<"20000921"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53
L17	1739	(32$bit and 64$bit and @pd<"20000921") and clock	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53
L18	38	((32$bit and 64$bit and @pd<"20000921") and clock) and asymmetric	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53
L19	115	dual adj edge and data adj transfer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53
L20	33	(dual adj edge and data adj transfer) and @pd<"20000921"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53

L24	243	(data adj transfer) with (rising adj edge) with (falling edge)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53
L25	120	((data adj transfer) with (rising adj edge) with (falling edge)) and @pd<"20000921"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53
L26	47	(data adj transfer) with (leading adj edge) with (trailing adj edge)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53
L28	3982	tate.inv.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53
L29	55	(tate and michael).inv.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53
L30	3	data adj word adj grouping	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53
L31	1	data adj bus adj grouping	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53
L32	1893	Media adj access adj controller	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53
L33	115	(Media adj access adj controller). ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 17:09
L34	992	(network adj switch).ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53
L35	0	I?O adj device adj simulat	USPAT	OR	OFF	2005/01/04 16:53

L36	1	I?O adj device adj simulation	USPAT	OR	OFF	2005/01/04 16:53
L37	0	sample near (rising adj edge and falling adj edge) and concatenate	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53
L38	278	64?bit near4 (data adj word)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53
L39	49	64?bit near4 (data adj word) and rising adj edge and falling adj edge	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53
L40	6	64?bit near4 (data adj word) and rising adj edge and falling adj edge and concatenate	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53
L41	25	64?bit near4 (data adj word) and rising adj edge and falling adj edge and @ad<"20000921"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53
L42	2	64?bit near4 (data adj word) and rising adj edge and falling adj edge and @ad<"20000921" and 32?bit	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53
L43	2	"5950223".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53
L44	1974	statistics and CRC and memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53
L45	1012	statistics and CRC and memory and @ad<"20000921"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53
L46	230	statistics and CRC and memory and @ad<"20000921" and DRAM	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53

L47	1	statistics and CRC and memory and @ad<"20000921" and DRAM and DRAM.ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53
L48	2	"5950223".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53
L49	0	(dual adj edge and sample) and @pd<"20000921" and MAC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53
L50	0	memory and router and dual?edge and @ad<"20000921"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53
L51	10231	memory and router and @ad<"20000921"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53
L52	44	RAM near4 router and @ad<"20000921"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53
L53	9	router adj RAM	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53
L54	9	(router adj RAM) and @ad<"20000921"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53
L55	3	(RAM near4 router and @ad<"20000921") and router.ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53
L56	7188	switch and physical adj layer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53

L57	3467	(switch and physical adj layer) and @ad<"20000921"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53
L58	5	((switch and physical adj layer) and @ad<"20000921") and switch adj RAM	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53
L59	1	((switch and physical adj layer) and @ad<"20000921") and switch adj RAM and MAC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53
L60	30	packet adj switch and MAC adj device	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53
L61	14	(packet adj switch and MAC adj device) and @ad<"20000921"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53
L62	8	((packet adj switch and MAC adj device) and @ad<"20000921") and RAM	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 16:53
L63	1	L33 and 710/30,62,65,66.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 17:09
L64	0	713/365,370.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 17:11
L65	115	media adj access adj controller.ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 17:14
L66	3	L33 and 709/250.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/04 17:15